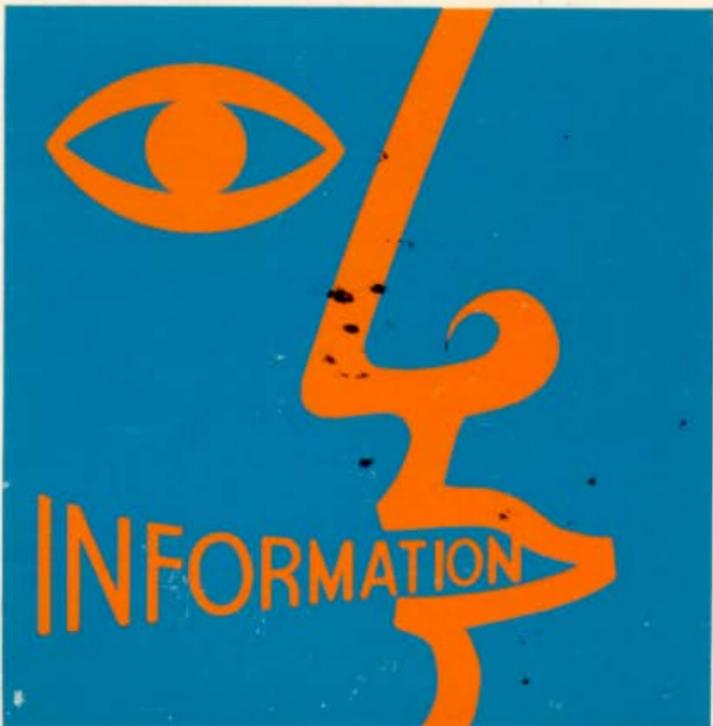


**IBM**

**3340**

Service Aid Manual

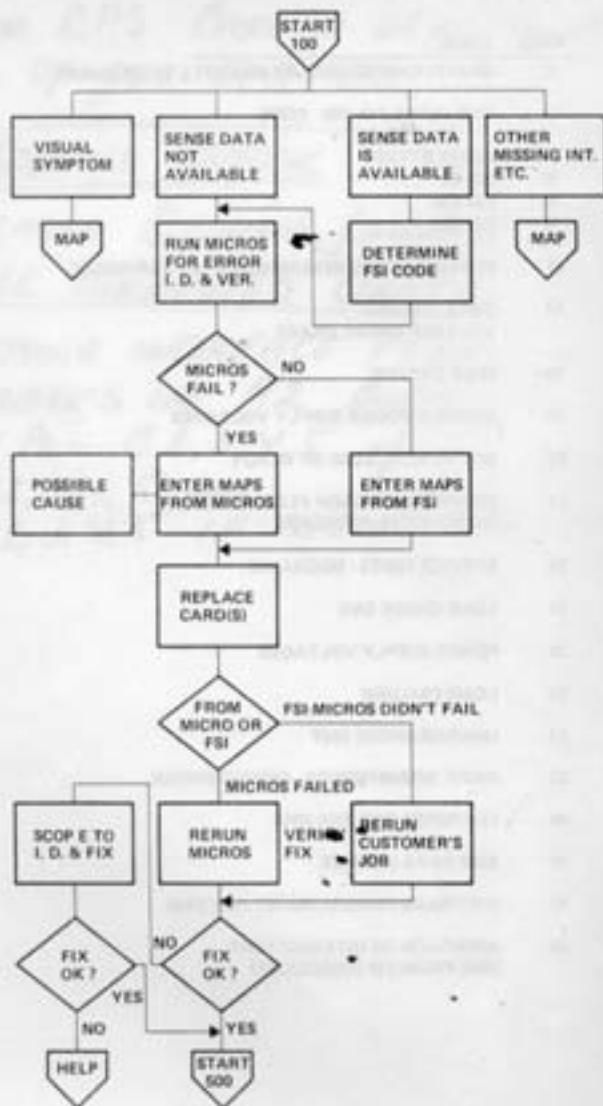
**SAM**



## CONTENT

PAGE	TITLE
2	MAINTENANCE LIBRARY MANUELL FLOW CHART
3	3340 QUICK FIX FSI - CODE
	SENSE BYTES
5	FSI 65
6	FSI 100
9	FSI 101
11	SERVICE AID TO INTERCHANGE A + B SPINDLES
13	DATA CHECKS/ VOLTAGE CHECK CHART
19	SEEK CHECKS
20	CHART A POWER SUPPLY VOLTAGES
23	NOT READY - LOSS OF READY
24	SERVICE APPROACH FLOW CHART MECHANICAL PROBLEMS
26	SERVICE HINTS - MECHANIC
27	LOAD CHECK OUT
28	POWER SUPPLY VOLTAGES
29	LOAD FAILURE
31	UNLOAD CHECK OUT
32	MECH. ADJUSTMENTS - DEPENDENCIES.
35	FEATURES FOR THE 3340
38	3340 DATA MODULE
39	SOFTWARE PROGRAMMING FOR 3340
48	APPROACH TO INTERMITTENT 3340 PROBLEM (CHECKLIST)

## MAINTENANCE LIBRARY MANUELL FLOW CHART



## 3340 QUICK FIX

F. S. I.	ERROR DESCRIPTION	POSSIBLE CAUSES
10XX	DEVICE INTERFACE CHECK	CABLES/CONN, A1C2, A2F2
11XX	DM SEQ CHECK	A1M2 (IL2), A1T4 (S4) CABLES & SWITCHES
12XX	ACCESS TIMEOUT	A1P2 (N2)
1301	SECTOR COMPARE CHECK	A1G2 (RPS ONLY)
1310	DRIVE CHECK (FALSE)	A1P2 (N2)
14XX	READ/WRITE SAFETY	RD/WR MATRIX A1H2, A1J2
15XX	OVERSHOOT CHECK	SERVO GAIN (MICROA7) SERVO CARDS HOME PHOTO SENSOR, A1P2 (N2)
160E	SERVO OFF TRACK	SERVO CARDS BOBBIN BINDING A SIDE A1R2, A1R4, A1T2, PWR. AMP., BSIDE Q2, Q4, S2, PWR, AMP,
16XX	SERVO OFF TRACK	A1P2 (N2)
1910	ERROR ALERT (NOT FURTHER DEF)	A2L2, A2F2, A2Q2
1911	TRANSMIT TARGET ERROR	A1G2 (RPS ONLY), A1C2
1913	TRANSMIT FIXED HEAD ERROR	A1F2 (E2), A1C2
1914	SYNC OUT TIMING ERROR	BUS TERM, A2F2 (A1A3)
1916	TRANSMIT CAR ERROR	A1J4 (ST SW ONLY), A1C2
1917-18	TRANSMIT HEAD DIFF ERROR	A1F2 (E2), A1C2
49XX	DATA CHECK-NO SYNC BYTE FOUND	RD WR MATRIX, A1J2, A2S2 (MAY BE BOTH MATRIX) A2T2, A2R4
9001	NO TAG VALID RD/WT OP	A2T2
9004	TIME OUT FOR INDEX	A1H2, A1F2 (E2)
9006	ECC HARDWARE CHECK	A2R4
	SERVO CARDS - A SIDE A1R2, A1R4, A1T2, PWR AMP - B SIDE A1Q2, A1Q4, A1S2, PWR AMP	
	VOICE COIL MOTOR P/N 2 745 700	

9009	BUSY MISSING AFTER SK START	A1P2 (N2)
900A	PHYS ADDRESS CHECK	DRIVE ADDRESS JUMPER, A1D2 SEE MLM INST 3
900F	ATTENTION CHECK	A1P2 (N2)
9104	I WRITE FAIL	A1H2, RD/WR MATRIX A2P2
91X8	CTL - I DEV - I BUS IN PARITY CHECK	A2F2, A1D2, CABLES & CONN
9110	DEV - I BUS IN PARITY CHECK	A1D2, CABLES & CONN
9120	ONE OF EIGHT CHECK	A1D2 + 6V - 4V NOISY
9180	CTL - I TAG BUS PARITY CH	A2K2 + 6V - 4V NOISY
91FC	CTL - I BUS IN ASSEMBLY FAILURE	A2K2
9200	CTL ERROR (FALSE)	A2K2, A2L2, A2F2
9202	ECC HARDWARE ERROR	A2R4
9204	MONITOR CHECK	A2L2, A2P2
9206	MONITOR-ECC HARDWARE ERROR	A2P2
9208-C	WRITE DATA-MONITOR CHECK	A2S2, A2P2, A2G2
921X	GAP COUNTER	A2P2 NOISY
922X	SHIFT REG	A2S2
9240	NO PLO	PLO CABLE, A1H2, A2T2
928X	PLO ERROR	A2T2

PROGRAM CONTROL DISPLAY HEX VALUE	MESSAGE B3 BYTE	DATA DISPLAY	0	1	2	3	4	5	6	7	DETAILED DESCRIPTION
E1	1	Physical Drive Identification	A	B	C	D	E	F	G	H	SENSE 106 Byte 4
E2	2	Sense HADR	Basic Dir In = 1 Not used FHFE	Basic DHF 256 FHFE	Fixed Heads = 1 Not used FHFE	Basic 32 FHFE	16	8	4	2	1
E3	3	Sense Difference Counter	128	64	32	16	8	4	2	1	
E4	4	Drive Status	Device ** Interface Check	Drive ** Check	Read/Write ** Check	Online *	Data Module Attention (D0 F)	Busy	Seek/Sector Complete		SENSE 106, 107 Byte 8
E5	5	Checks/ Status	Data Module ** Loaded Switch Latched	Sector ** Compare Check	Motor at ** Speed Latched	Air/Belt ** Switch Latched	Write Enable	Data Module Size 4 bit	Data Module Size 2 Bit (70 Mbit)	Data Module Size 1 Bit (35 Mbit)	SENSE 107 Byte 9
E6	6	Data Module Sequence Control	Data Module ** Size Check	Data Module * Latch 4	Data Module * Latch 2	Data Module * Latch 1	Check ** Latch	Data Module ** Sequence Check Latched	Bias Disable Switch	Odd Track	SENSE 107 Byte 10
E7	7	Load Switch Status	Drive Start * Switch	Data Module * Present Switch	Cover Locked * Switch	Data Module Unlocked Switch	Data Module * Loaded Switch	Air/Belt * Switch	Carriage Home	Motor at * Speed Switch	SENSE 107 Byte 11
E8	8	R/W Safety	Multiple ** Head Select Check	Capable / ** Enable Check	Write ** Overrun	Index ** Check	R/W ** Interlock Check	Control ** Check	Transition ** Check	Write ** Current Check	SENSE 107 Byte 12
E9	9	Access Status	Access ** Timeout Check	Overshoot ** Check	Servo Off ** Track Check	Track * Crossing	Servo * Latch	Linear * Mode Latch	Control * Latch	Wait Latch	SENSE 108 Byte 13
EA	10	Controller Checks	PLD ** Check	No PLD ** Input	SERDES ** Check	Gap ** Counter Check	Write ** Data Check	Monitor ** Check	ECC ** Check	ECC * Zeros Detected	SENSE 108 Byte 17
EB	11	Control Interface Checks	Control Interface Tag Bus Parity Check **	Control Interface Bus Out Parity Check **	Device ** Selection Check	Device Bus In Parity Check **	Control Interface Bus In Parity Check **	Write ** Fail			SENSE 108 Byte 20, Bits 0-5
EC	12	Device Interface Checks							Device Bus Out Parity Check **	Device Tag Parity Check **	SENSE 108 Byte 20, Bits 6, 7
ED	13	Target Address Register (RPS Feature)	(Bit D = 1 if RPS installed)	64	32	16	8	4	2	1	
EE	14	Sense Cylinder Address Register (Switch Feature)	256	128	64	32	16	8	4	2	
EF	***15	Status (FHFE Only)	Direction Bit 1 = IN		Difference 256			Low Gain Error **		Fixed Head Feature Installed	SENSE 108 Byte 19
CE		Routine Number	1	0	1	1	0	0	1	1	MICRO 36

\* Indicators which are normally on with no error condition, Ready lamp on, and DM sequence at State 6.

\*\* Error or check conditions.

\*\*\* This byte for FHFE only.

## SENSE DATA SUMMARY (5 pages)

**BYTE**

**BIT**

	0	1	2	3	4	5	6	7
0	Command Reject	Inconsistent Required	Ctrl Bus Out Parity	Equipment Check (byte 21)	Data Check (byte 21)	Overrun	Tra Condition Check	Seek Check (byte 21)
1	Permanent Error (byte 21)	Invalid Tria Format	End of Cylinder	Unused	No Record Found	File Protected (not used with System 3 DSA)	Write Unprotected	Operation Incomplete
2	MPS Feature Present	Convertible	Unused	Environmental Data Present	Unused	Data Module Size = 128 Fixed Head	Data Module Size = 128 - 1024 Bytes (byte 21)	Data Module Size = 2048 Bytes (byte 21)
3	RESTART COMMAND (Provided only when byte 1 bit 7, Operation Incomplete, is active)							
4	PHYSICAL DRIVE IDENTIFICATION							
5	128	64	32	16	8	4	2	1
6	LOW ORDER LOGICAL CYLINDER ADDRESS and HIGH ORDER LOGICAL CYLINDER ADDRESS							
7	112	256	Log Trk 0	Log Trk 4	Log Trk 2	Log Trk 1	FORMAT (bits 0-3 hex)	MESSAGE CODE (bits 4-7 hex)

OR

0, 1, 2, 3, 4, 5, 6, 7
• CURRENT SEEK ADDRESS (128, 64, 32, 16, 8, 4, 2, 1)
• CURRENT SEEK ADDRESS (128, 64, 32, 16, 8, 4, 2, 1)
If Seek Check active (byte 0, bit 21)
• Current seek address is the last argument (address issued to the device). Byte Format remains the same as at right.

Note: See Storage Control MLM for sense data and/or messages for Formats 0, 2, and 3. If the storage control is a System 3-3340 DSA, refer to the 3340 DSA T-MD manual for Formats 0 and 2.



Note:

- Set by ERPs.
- Usually indicates a 3340 malfunction if byte 7, bits 0-3 equal '1', '4', or '5'.
- If the storage control is a System 3-3340 DSA and a 12 MB CE data module is installed, both bits 6 and 7 are zero.

SENSE BYTES 8-23

MESSAGES

To formats 1, 4, 5, 6

3340	AV9001 Rev. 2 of 2 Part No. 1	2747347 25 Jun 73	440200 2 Nov 73	440203 13 May 74	440213 8 Aug 74	440218 14 Mar 75	440223
------	-------------------------------------	----------------------	--------------------	---------------------	--------------------	---------------------	--------

© Copyright IBM Corporation 1973, 1974, 1975

MESSAGES, de  
FORMAT

0	No Message
1	Transmit Target Error
2	Microprogram Inhibited by byte 1
3	Transmit TSB Err
4	Sync Out Timing Error
5	Unexpected Drive Status or Initial Selection
6	Transmit Cylinder Address Error
7	Transmit Head Error
8	Transmit Difference Error
9	Drive Status Not As Expected During Read IPL
A	Seek Verification Check on Physical Address
B	Seek Incomplete or Sector Non-Complete
C	No Interrupt From Drive
D	Defect Skipping Re-orientation Check
E	DMF Incompatibility Inhibit DM Size
F	Not used

FORMAT 1

# SENSE DATA SUMMARY SENSE 100

MESSAGES, determined by format and message code (Byte 7)

FORMAT 1      FORMAT 4      FORMAT 5

MESSAGE CODE (bytes 7, bits 4-7 Max)	FORMAT 1	FORMAT 4	FORMAT 5
0	No Message	RA Field data check	Not used
1	Transmit target error	Count field data check	Not used
2	Microprogram download errors identified by byte 101	Key field data check	Not used
3	Transmit TSP Error	Data field unrecoverable data check *	Data field unrecoverable data check *
4	Sync Out timing error	RA Field no. sync byte found	Not used
5	Unspecified drive status at initial selection	Count field no. sync byte found	Not used
6	Transmit Cylinder Address Error	Key field no. sync byte found	Not used
7	Transmit head error	Data field no. sync byte found	Not used
8	Transmit difference error	Not used	Not used
9	Drive status not as expected during Read (PLI)	Not used	Not used
A	Seek verification check on physical address	Not used	Not used
B	Seek incomplete or Sector Non-Compare	Not used	Not used
C	No interrupt from drive	Not used	Not used
D	Detector missing transmission check	Not used	Not used
E	DM Incompatibility/ invalid DM size	Not used	Not used
F	Not used	Not used	Not used

FORMAT 1      FORMAT 4      FORMAT 5

# SENSE DATA SUMMARY SENSE 100

To add or remove cards it is recommended that power turned off. If this is impractical, add or remove cards via the following key:

- For cards C2, D2, G2, H2, J2, and R/W Matrix - must always turn power off.  
Turn both drives off line.
- For cards F2 (E2), M2 (L2), P2 (N2), T2 (S2), T4 (S4), O4(R4) and power who.  
One or both spindles - perform the following steps:  
 A... Unload the data module (for customer data integrity).  
 B... Place the drive in the CE mode (to prevent generating errors or interrupt).  
 C... Turn the +24v, pschf off at the CE panel.  
 Turn the -36v, CB off. A drive + CP 408 (to prevent circuit damage).  
 B drive + CP 407

OIA - AI DRIVE BOARD									
A	B	C	D	E	F	G	H	J	K
Y1 CE MATRIX A DRIVE/B DRIVE	Z1 R/W MATRIX B DRIVE	L	M	N	P	Q	R	S	T
V1 R/W MATRIX A DRIVE/B DRIVE	Z2 R/W MATRIX A DRIVE	U	V	W	X	Y	Z	AA	AB
1	2	3	4	5	6	7	8	9	10
Y1 R/W MATRIX A DRIVE/B DRIVE	Z1 R/W MATRIX B DRIVE	1	2	3	4	5	6	7	8
DATA/PLD IN	DATA/PLD OUT								
BEV-1 IN	BEV-1 OUT (TERM) OUT	BEV-1 IN	BEV-1 OUT	BEV-1 IN	BEV-1 OUT	BEV-1 IN	BEV-1 OUT	BEV-1 IN	BEV-1 OUT
6	7	8	9	10	11	12	13	14	15
* FEATURE CARD									

# OIA-A2 CONTROLLER BOARD

	A	B	C	D	E	F	G	H	J	K	L	M	N	P	Q	R	S	T	U	V	
	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
DATA/FLO IN																DEV INT IN	DEV INT IN				
																CE PNL OUT	CE PNL IN				
FLO/VFO																					
SERDES																					
ECC																					
MACROS																					
CAP CTR/CONTROLS																					
*SWITCH COMMON																	SW FE				
BUS/DP CTRLS/EE DISP																					
ASSM BUS/RESPONSE																					
*STATUS B INT																	SW FE				
*STATUS A INT																	SW FE				
POLLING/SELECTION																					
BUS IN/BUS OUT/BI ASSM																					
*SELECT B INT																	SW FE				
*SELECT A INT																	SW FE				
CTL-I																CTL-I					
*CTL-IB																*CTL-IB					
SW FE																SW FE					
*CTL-IA																*CTL-IA					
SW FE																SW FE					
	24		m														67				

STRINGS  
SWITCHES  
SWITCHES

+ FEATURE

## FORMAT 1

## SENSE DATA SUMMARY (continued)

	BIT												
BYTE	0	1	2	3	4	5	6	7					
Drive Status	8	Device Check ■	Device Interface Check ■	Drive Track Check ■	Device Interface Check ■	On Line ■	Data Module Address	Run					
Checksum	9	Data Module Search Latched ■	Device Control Check ■	Motor or Speed Latched ■	Aut/Sel Search Latched ■	Write Track	Data Module Bus 7 Bit Fixed Head	Data Module Bus 7 Bit Fixed Head					
DM Sequence	10	Data Module Bus 8 Bit Fixed Head	Data Module Latch 20 ■	Data Module Latch 1 ■	Check Latch ■	Data Module Interface Check Latched ■	Run Status	Deep Track					
Load Status	11	Drive Start Search ■	Drive Module Aut/Sel Search ■	Cover Locked Search ■	Data Module Unlocked Search ■	Data Module Aut/Sel Search ■	Carry Home	Motor or Speed Search ■					
R/W Safety	12	Motor Head Driver Check ■	Driver Check ■	Write Driver ■	Index Check ■	W/R Interlock Check ■	Control Check ■	Transistor Check ■					
	13	CONTROL INTERFACE BUS OUT (For Message Code 0) (For Message Code 2, See ■)				EXPECTED DRIVE STATUS/DATA (For Message Codes 1,3,5,6,7,8,9)							
	14	CONTROL INTERFACE BUS IN (At the time an error was detected)											
	15	CONTROL INTERFACE TAG BUS (At the time an error was detected)											
Access Status	16	Access Time Out Check ■	Overhead Check ■	Service On Track Check ■	Track Crossing ■	Service Limit ■	Lower Mode Limit ■	Control Limit ■					
Controller Checks	17	PDU Check ■	No PDU Input ■	SERVO1 Check ■	Over Counter Check ■	Write Goto Check ■	Motor Driver Check ■	SOC Check ■					
Micro Generated Errors	18	CODED ERROR CONDITION (bits 4-7 hex)											
Status	19	Set R/W bit Low Seek Error ■ Fixed Head Position											
Interface Checks	20	Control Interface Tag Bus Party Check ■	Control Interface Bus Bus Party Check ■	Drive Selection Check ■	Control Interface Bus Bus Party Check ■	Drive Jumper J ■	Device Bus Out Party Check ■	Device Tag Party Check ■					
Sense From Drive	21	FAULT SYMPTOM CODE											
	22	FAULT SYMPTOM CODE											
	23	FAULT SYMPTOM CODE											

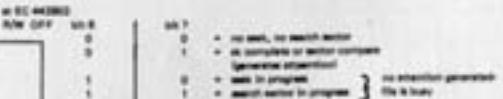
■ Indicates an error condition detected.

■■ Indicates bits normally on with no error conditions. Ready lamp on and DM sequence at State 0.

3340

AIV1000	Z147349	A46200	A46202	A46203	A46213	A46218	A46223
Rev. 1 of 1	Rev. 1 of 1	26 Jun 73	27 Aug 73	2 Nov 73	12 Nov 74	8 Aug 74	14 Mar 75

© Copyright 1984 Datavation Inc., 10101, 10102



## SENSE DATA SUMMARY FORMAT 1 (CONTINUED) SENSE 101

OR

1	Write	Read	Active Track
---	-------	------	--------------

If Set R/W is active (Byte 18, bit 0)

Message upon Error  
Message, determined  
by Sense Byte 18, bits 4-7

0 Unset

1 No Tag Valid on R/W Or ■

2 No Response or Change End on  
R/W Or on R/W Or on SOC Or3 No response from controller  
on Control Or4 Time out waiting for Index or  
Active Track

5 SOC Hardware Check ■

6 Message or no connection  
selected7 Protection Check/Short  
Bus Time Expire8 Head Switch Timer Exceeded  
check9 Bus missing after  
Seek Start in itself

A Physical Address Check

B-E Unset

F Attention Check

OR

0	1	2	3	4	5	6	7
---	---	---	---	---	---	---	---

■■■PREVIOUS SEEK ADDRESS

Low Logical Cylinder Address      High Logical Cylinder Address

If Seek Check is active (Byte 8, bit 0)

■■■Previous seek address is the  
address at which the drive  
was located prior to the last  
issued seek argument bytes  
0 and 1.

OR

0	1	2	3	4	5	6	7
---	---	---	---	---	---	---	---

■■■PRESENT ADDRESS (Read from disk)

Low Logical Cylinder Address      High Logical Cylinder Address

If Seek Check active (Byte 8, bit 1)

■■■PRESENT ADDRESS (Read from disk)

High Logical Cylinder Address      Low Logical Track Address

■ Bytes 13, 14, and 15 will be  
valid for microprogrammed error  
messages described by ■

## SENSE 101

The table on page 10 provides you with the EC development of the 3340 Electronics, the appropriate Card Partnumbers and 3340 MLM Level.

1. Find vertical column = 3340 MACH. EC<sup>11</sup>

2. Follow horizontal = 442000<sup>11</sup>, now until you cross column  
= CAR JA<sup>11</sup>. On that point you find 6899

3. Complete PN by adding first 3 figures

Complete 823 PN to 6899 and get 823 6899

Example:

What PN should be in Pos. A114 on a 3340 with EC 442000?

## 3340 SERVICE AID TO INTERCHANGE A+B SPINDLES

**ABSTRACT:** On a single drive failure - swap electronics between drive for failure isolation.

**TEXT:** To help isolate a Read/Write or Servo problem to the electronics, cables and connectors, or physical spindle, interchange electronics between the A and B spindles of a 3340 A2 or B2 box.

Before this procedure is used:

1. The 3340 MLM maintenance procedure should have been exhausted.
2. The data module should have been eliminated as a source of problem.
3. R/W matrix cards on both A and B drive replaced - not interchanged with each other.
4. Positive error definition, that is:  
Microdiagnostic error stops or forced error with CCW string (use FRIEND and obtain sense data) or a repeatable customer program - error indication.

After Each test step # below, do the following items (A, B, C)

- A. Rerun failing test.
  - B. If trouble moves to other spindle, skip to step 7.
  - C. If trouble remains with same physical spindle, go to next step.
1. With Power-off, interchange cables: (See Figure 1)  
**CAUTION:** Be sure cables are seated and no loose connectors exist.

B Drive — with — A Drive      Description

A1U2	A1V2	Servo Pre AMP signal
A1U3	A1V3	Servo Power AMP drive
A1U4	A1V4	DM sequence
A1U5	A1V5	Drive Switches
A1Y3	A1Y4	Upper R/W matrix - A2
A1Z1	A1Z2	Lower R/W matrix - A3

This effectively interchanges all electronics between A and B drives at the AT board. Note that CE switch - B must be on to run physical spindle - A and vice versa. Also customers logical addresses have been reversed. Check servo is within tolerance - Run A7 micro (cards A182, 001)

2. If trouble moves to other physical spindle: Problem is in MST cards or A1 board - try replacing board if all cards have been swapped. (Skip to step 7 to complete analysis when trouble moved).
 

If trouble remains with same physical spindle: Eliminate the power AMP and power AMP drive Cables by:

    - A. Interchanging A1U3 with A1V3 (back to original positions) and
    - B. Interchange large lead on top of VCM coil from A to B spindle.
  3. If trouble remains with same physical spindle: Eliminate R/W matrix flat cables by interchanging:
    - A. A1Y3 with A1Y4 (back to original positions)
    - B. A1Z1 with A1Z2 (back to original positions)
    - C. R/W matrix connecting blocks and pair of cables
  4. If trouble remains with same physical spindle: Eliminate servo pre-AMP signal cable by interchanging:
    - A. Cables R/W matrix connector - drive connector plug see MLM R/W 350 A. to B drive
    - B. Cables A1U2 with A1V2
  5. If trouble remains with same physical spindle, interchange (VCM) voice coil motor or bobbin ASM.
  6. If trouble remains with same physical spindle,

Suspect:

  - A. DM Sequence cable A1U4/A1V4
  - B. Drive switches cable A1U5/A1V5
  - C. Drive mechanical problems
  - D. Environmental problem ESD/Noise
  7. Diagnosis is complete. Return all cables to original positions and verify proper operation of non failing drive. Take corrective action on failing drive. Verify its proper operation.

## 3340 DATA CHECK ANALYSIS PROCEDURE (OUTLINE)

A	What type of data checks?	
1.	Sense byte 7 = 53	Data field ECC correctable data checks.
2.	Sense byte 7 = 4X	ECC uncorrectable data checks
	Symptom Code	Micro B-1 Err
1.	4940	B1F0
2.	4941	B1F1
3.	4942	B1F2
4.	4943	B1F3
5.	4944 *	B1F4 *
6.	4945	B1F5
7.	4946	B1F6
8.	4947	B1F7

This is the error that will occur if unable to read at all.

- 8 What is failing ?  
 (Data Module ? Drive ? Drive Module ? Controller ? )

  1. Check all available sense information for
    - a) Symptom Code 494X or Byte 7 = 4X SENSE 100
    - b) Physical Drive ID – Byte 4 = 0-7 MSG 30 - 42
    - c) Environmental Data from Logout to determine Data Module ID (s) MSG 32 - 40
  
  2. Try to recreate the failure and gather more information by doing one or more of the following:
    - a) Run DLTS 3340 PSA and 3340 PSB OLT 22 - 24
    - b) Run Micro B1 in default mode MICRO 34
 

A Read operation will be tested on all physical heads.
    - c) Run Micro B1 on a failing customer track to determine if failure can be recreated with Micro B1. MICRO 34
    - d) Run B2, insure you do not have write problem. MICRO 35
    - e) Run AF iron failing test. MICRO 32

- Swap data modules around and perform step 2 on different drives until satisfied that the problem has been isolated to the data module, drive, drive module or controller.
- If the controller is the only module in the subsystem and both drives are failing, assume that the drive module is failing for Map Entry purposes. If one data module is failing on one drive, if one data module is failing, first assume that the Data Module is failing. Then assume that the drive is failing. R/W 300.

**Note: Data Module**

The absolute minimum amplitude should be no less than 40 millivolts on the innermost cylinders. Good data modules should normally show 50 millivolts and above, nominal 150 mV.

To ensure that low amplitude is being caused by the media, scope the envelope amplitude on:

- different heads on inner and outer cylinders,
- different data modules on failing drive,
- failing data module on different drives.

**C Which map ?**

- Data module (R/W 340)
- Drive (R/W 302)
- Data module and drive (R/W 340)
- Drive module (3340 box) (R/W 304)
- Controller (R/W 306)
- Drive module or controller (R/W 304) (A02 only installed)

**D Which Scoping Procedure ?**

If card replacement does not fix the problem or scoping is required to further isolate the problem, check servo stability: Do track following scope + position signal (A-drive: A1T2 D69, B-drive: A1S2 D09, max. 1Vpp).

Appropriate scope procedure for:

- Data module (R/W 347)
- Drive (R/W 303, R/W 309)
- Drive module (3340 box) (R/W 308)
- Controller (R/W 320 - 326)  
MICFL 1193 - 1196

**E Last Chance**

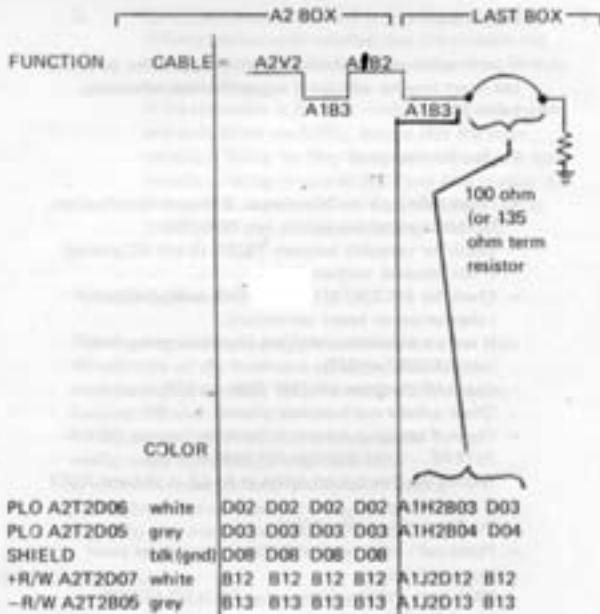
If card replacement and scoping are not applicable, go to the last resort map for additional suggestions and references. (R/W 312).

**Further Service Hints**

- Check AC-ripple on DC-voltages. If exceeds specification limit take corrective actions (see PWR 290).
- Check for capacitor between TB101-16 and AC ground. If not installed, contact ASG.
- Check for BM 2347381, EC 445047, being installed (clips on power board connectors).
- If not yet installed or shipped, place emergency order (see TIP 3340 wt 044).
- Check VFO adjustment (TIP 3340 wt 039).
- Check spindle and baseplate ground.
- Check if last drive module in the string has new DEV. - INTERF. - CABLING (see TIP 3340 wt 046). Unplug the 3 outbound cables at A1A3, A1A4 and A1B2 if necessary.
- Pre Filter dirty (DC - BOX).
- Photo cell - cable pinched by black appearance panel.
- Voice coil motor.
- PWR Amp / VCM cabling noisy. Fix is EC 442828.
- Check R/W, PLD cables.

A quick check of the R/W and PLD cables can be made with an ohmmeter (see R/W 306).

- Turn power OFF and remove card A2T2
- Measure each signal lines' ability to reflect the 100 OHM termination resistor at the other end of the line (jumped on last 3340 in string during installation). Using an ohmmeter measure the resistance of A2V2 pins D2, D3, B12, B13 to ground; a quick check of the resistance of each pin should be identical. For multiple attached 3340's see Chart Page 16.
- Meter the resistance at A2V2 between each pair of signal lines D2 to D3 then B12 to B13, to insure they are not shorted.
- If noise is suspected, check the integrity of the ground shield. Pull each cable end starting at A2V2 (shown in chart). Meter cable pin D08 to board ground pin D08 for 0 ohm. Replace cables and repeat steps 2 and 3 above.
- Replace card A2T2.



SIGNAL TO GROUND RESISTANCE CHECK (nominal value, with 100 ohm terminator)

# 3340's INSTALLED	NO TERMINATOR	WITH TERMINATOR
1st 3340	220	68
2nd 3340	110	52
3rd 3340	73	42
4th 3340	55	35

OHMS → NOMINAL RESISTANCE

### TRANSFORMER PRIMARY INPUT TAP WIRING CHART (Mod B02)

Voltage	Tap 201 (TB 030)
200 V	Lead #1 to TB 311-2
	Lead #2 to TB 311-7
220 V	Lead #1 to TB 311-2
	Lead #2 to TB 311-6
235 V	Lead #1 to TB 311-1
	Lead #2 to TB 311-6
260 V	Lead #1 to TB 311-2
	Lead #2 to TB 311-6
408 V	Lead #1 to TB 311-1
	Lead #2 to TB 311-5

DC Supply	Test Point	Transformer (TB) #	Admittance	Load	Minimum AC Resistor	Output	Map Entry
-24 V Line	12V V.A Switch Ground Term	-21.6 to -26.80	None*	100000	0.150 V 610	PRW 211	PRW 211-A
-24 V	A1L2005	-24.0 to -28.8	None*	100000	0.18 V 610	PRW 261	PRW 261-A
-13 V	A1R2006	-13.0 to -18.4	None*	100000	0.15 V 610	PRW 241	PRW 241-A
-13 V	A1M2006	-13.0 to -18.4	None*	100000	0.15 V 610	PRW 241	PRW 241-A
-4 V	A1P2006	-3.72 to -8.4	None*	100000	0.18 V 610	PRW 208	PRW 208-A
16 V Reg	A1J2B11	0.876 to 0.878	1+	100000	0.38 V 610	PRW 201	PRW 201-A
-36 V	TR1012	-36.0 to -43.2	None*	100000	0.14 V 610	PRW 361	PRW 361-A
-36 V	A1T2B15	-36.0 to -40.4	None*	100000	0.17 V 610	ACC 370	ACC 370-A
-36 V	A1S2B10	-36.0 to -36.4	None*	100000	0.21 V 610		

### VOLTAGE CHECK CHART



\* Check transformer primary taps, change to match available entries  
\*\* To increase the voltage, turn the adjustment screw clockwise (CW)

## TRANSFORMER PRIMARY INPUT TAP WIRING CHART (Mod A82)

	Voltage	TB206 (YA218)	TB211 (YA209)
Delta	200 V	Load #1 to TB206-2	Load #1 to TB211-2 Load #2 to TB211-7
	220 V	Load #1 to TB206-2	Load #1 to TB211-2 Load #2 to TB211-8
Wye	208 V	Load #1 to TB206-4	Load #1 to TB211-1 Load #2 to TB211-5
	226 V	Load #1 to TB206-3	Load #1 to TB211-2 Load #2 to TB211-6
	240 V	Load #1 to TB206-4	Load #1 to TB211-1 Load #2 to TB211-5

DC Supply	True Power	Transformer Output	Admittance	Load	Mains	Diagnos	MAP-Edge
+5V Reg	TB401 (ACC 540, Rev 1)	+19.2 to +20.1	None*	YAD001	0.5 V 0.0	PIN 21	PIN 20-A/C
+24 V Load	+24 V Servo A Carrie Board	+21.8 to +26.40	None*	YAD001	0.380 V 2.0	PIN 71	PIN 70-A
-24 V	A1120101	-24.0 to -23.8	None*	KY0110 YAD001	0.08 V 0.0	PIN 51	PIN 50-A
+12 V	A1020006	+12.0 to +11.8	None*	KY0110 YAD001	0.115 V 0.0	PIN 41	PIN 40-A
-12 V	A1020006	-12.0 to -11.8	None*	KY0110 YAD001	0.115 V 0.0	PIN 41	PIN 40-A
+4 V Reg	A1 Board PZ0006	+3.8 to +4.10	+/-	KY0110 YAD001	0.08 V 0.0	PIN 61	PIN 60-A
-4 V Reg	A1 Board K2006	-3.8 to -4.10	+/-	YAD001	0.14 V 0.0	PIN 61	PIN 60-A
+8 V Reg	A1 Board J7911	+7.8 to +6.24	+/-	KY0100 YAD001	0.08 V 0.0	PIN 81	PIN 80-A
-8 V Reg	A1 Board K2011	-7.8 to -8.24	+/-	YAD001	0.08 V 0.0	PIN 81	PIN 80-A
+16 V Reg	J7910-3	+16.0 to +15.7	None*	YAD001	0.14 V 0.0	PIN 81	PIN 80-A
-16 V	A17810	-16.8 to -20.4	None*		0.01 V 0.0		ACC 720-A
-20 V	A152010	-16.8 to -20.4	None*		0.01 V 0.0		ACC 720-A

\* Check transformer primary input taps, change to switch isolatable voltage, set after diagnosis.

+1-4 V and 16 V should be adjusted to -4.8 V and 16.0 V using the test point on the adjustment trimmer module (DM).

## SEEK CHECKS

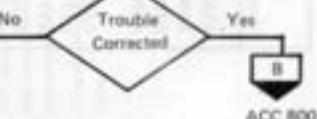
Failure not found by MAPs or duplicated by MICROs.

## Possible Causes

1. Power Supply specifications. – Refer to Chart A. (Page 28).
2. Velocity Gain Adjustment. – Refer to ACC 800 Entry B.
3. Data Module. – Swap if available.
4. A1T2 (A152) Servo Amp (see Note). – Refer to ACC 440 for card verification procedure.
5. A1R2 (A1Q2) Servo Analog (see Note). – Refer to ACC 450 and ACC 640 for card verification procedures.
6. A1R4 (A1Q4) Servo Logic (see Note). – Refer to ACC 460 and ACC 650 for card verification procedures.
7. P410 (P411) Power Amplifier (see Note). – Refer to ACC 470 and ACC 660 for card verification procedures.
8. A1P2 (A1N2) Access Control.
9. A1F2 (A1E2) Head Select.
10. A1C2 Receiver and Controls.
11. A1D2 in Bus and Select.
12. Cable from data module to A1V2 (A1U2). – Refer to ACC 430 for configuration. (Check for proper shield ground and Servo Input isolation).
13. Cable from P410 (P411) to A1V3 (A1U3). – Refer to ACC 430 for configuration. (Check for proper shield ground).
14. Check card and cable plugging for location and secure installation.
15. Check card grounding network according to the procedure and diagram on ACC xxx.
16. Verify drive grounding network according to the procedure and diagram on ACC 701.
17. Verify track following servo operation according to the procedure and diagram on ACC 701.
18. Inspect Voltage TB's and edge connectors for loose connections of fraying wires.
19. Inspect DM Load area for loose hardware or foreign matter.
20. Scan Microfiche for Service Aids or Engineering Changes which might correct the problem.
21. Replace Home Detector photo cell.
22. MAKE SURE THAT NO VCM CAUSE PROBLEM
  - Bobbin; loose coil/clamping screws/washers secure screws with loctite. (SEE DM 845).
  - Bobbin height adj. nut loose, secure with loctite and readj. (SEE DM 770)
  - Crash stop broken (SEE DM 845)
  - VCM is located against pin (on left front side of motor).
23. Check if Carriage Latch Actuator Adj is too high DM 755

Refer to rezero Operation, Theory (ACC 400) or Seek Operation Theory (ACC 600)

\* Note on page 20



## CHART A - POWER SUPPLY VOLTAGES

Note: Card replacement should be followed by Velocity  
 Gain calibration before retesting drive operation.  
 Refer to the procedure on ACC 800 Entry B.

Measuring Point	Voltage Range	Maximum Ripple	Map Exit	Satellite Module
			Control Module	
A1R2D03 (020003)	-24.0 to -20.0 Vdc	0.08 V p-p	PWR 50 - A	PWR 250 - A
A1R2D05 (020005)	+12.0 to +14.4 Vdc	0.10 V p-p	PWR 45 - A	PWR 245 - A
A1R2D06 (020006)	-12.0 to -14.4 Vdc	0.10 V p-p	PWR 40 - A	PWR 240 - A
A1R2G06 (020006)	-3.84 to -4.16 Vdc	0.04 V p-p	PWR 05 - A	PWR 255 - A
A1R2G11 (020111)	-3.72 to -4.4 Vdc	0.16 V p-p	PWR 60 - A	PWR 260 - A
A1T2B08 (52B08)	+5.76 to +6.24 Vdc	0.08 V p-p	ACC 441	ACC 441
A1T2B10 (52B10)	-1.28 Vdc (internal)	—	ACC 720 - A	ACC 720 - A
P4.10-7.8 (P411-78)	-19.6 to -20.4 Vdc	0.07 V p-p	PWR 80 - A	PWR 280 - A
P4.10-14 (P411-14)	-26.0 to -42.2 Vdc	0.14 V p-p	PWR 40 - A	PWR 240 - A
P4.10-16 (P411-16)	-12.0 to -14.4 Vdc	0.10 V p-p	PWR 45 - A	PWR 245 - A
P4.10-9.10 (P411-9.10)	+12.0 to +14.4 Vdc	0.10 V p-p	YA060 (YB060)	YA060 (YB060)
P4.10-13 (P411-13)	Ground	—	YA060 (YB060)	YA060 (YB060)

See LOC 4 for location of P410/411.

### Ready Conditions:

#### Initial

#### Operational (Normal)

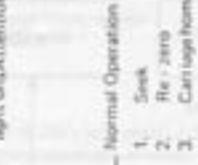
- Load & GO
1. Drive is selectable (note in C.E. mode)
  2. DM sequence complete data module ready (DM sequence state 6)
  3. Access complete (first time)



#### "Drop Ready" Symptom

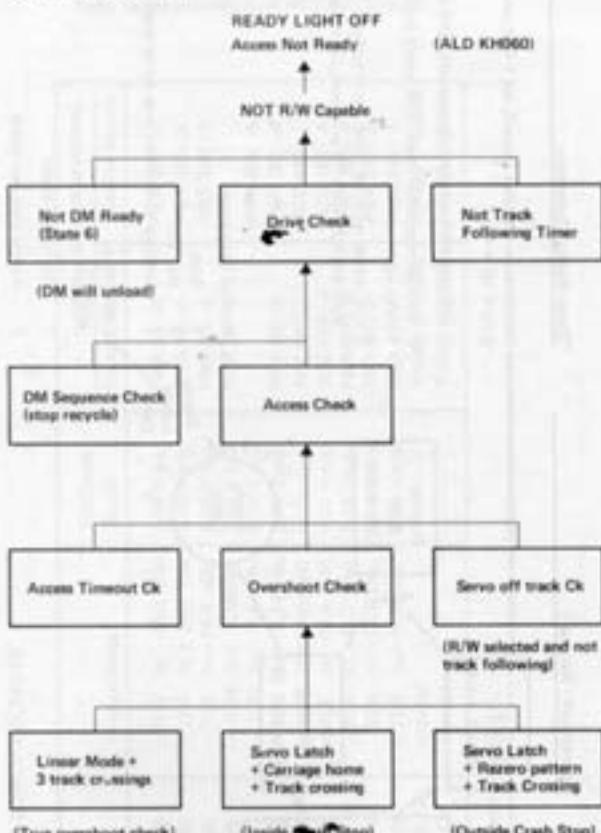
Intervention required byte 0, bit 1 first on line.  
 Byte 8 bit 4 = 0.)

1. DM sequence error while operating (DM unload)
2. Voltage transient - reset (DM won't unload - ready return)
3. C.E. mode latch on (DM won't unload - "ready" light on). Attention PG will reset CE mode latch



1. Seek
2. Re-zero
3. Can logic home/start stop w/o to input.

### SERVO ERROR LOGIC:



Normally caused by:  
 1. Improper adjustment of pot on A1Q2 or A1R2  
 2. Noisy Linear Mode while seeking

Normally caused by:  
 1. Carriage Home photo-cell coming on while seeking

### NOT READY - LOSS OF READY

NOTE: Loss of READY light is a by-product of the real error. Identify the real problem on your I.R., not just 'Drop Ready'. Identify the cause, not the effect:

1. Seek Check - Ready light out with the DM loaded.  
or
  2. DM Sequence Check - DM unloaded with the Start/Stop switch still ON.
1. If Drive is in failing condition run Inline routine B3 see Micro to determine cause
  2. No trouble found check:
    - DM - serialnumber, Internal DM's with serial # 73 . . . . 0 - . . . . 290 may cause intermittent loss of ready
    - DM - Loaded switch is at very end of cam-ramp. Readj see DM 730 effected serial #: A02 10200 - 10900  
B02 30120 - 30650  
B01 50030 - 50150
    - Poweramp cooling ribs must not have contact to each other
    - Access- velocity should be 20V > 2%. Readj if necessary
    - Drive Motor thermal tripped, (CP 217 or CP 218)

### DATA MODULE

#### LOAD/UNLOAD

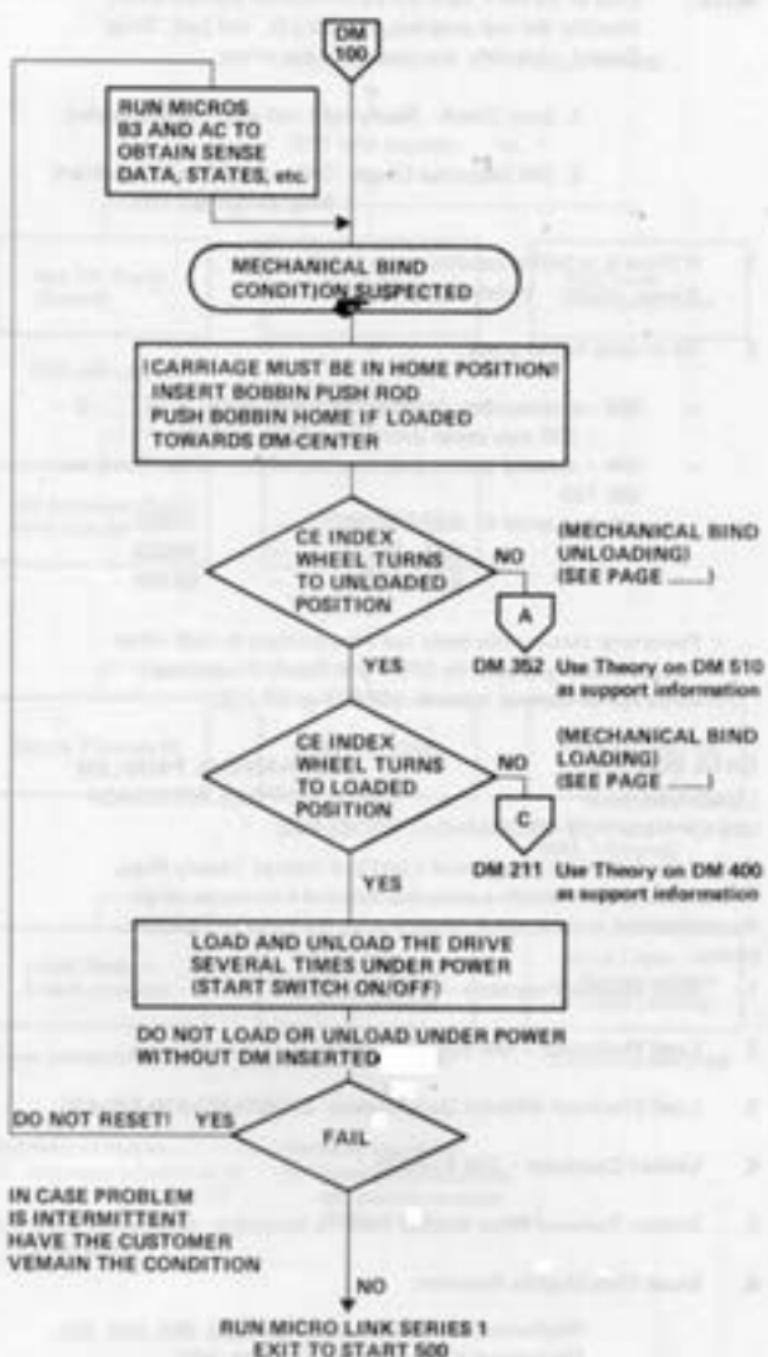
#### USE OF MLM FOR MECHANICAL PROBLEMS

Review the following: Mechanical Load and Unload Theory Maps. This is necessary to obtain a complete detailed knowledge of all the mechanical actions which occur during the Load and Unload cycles.

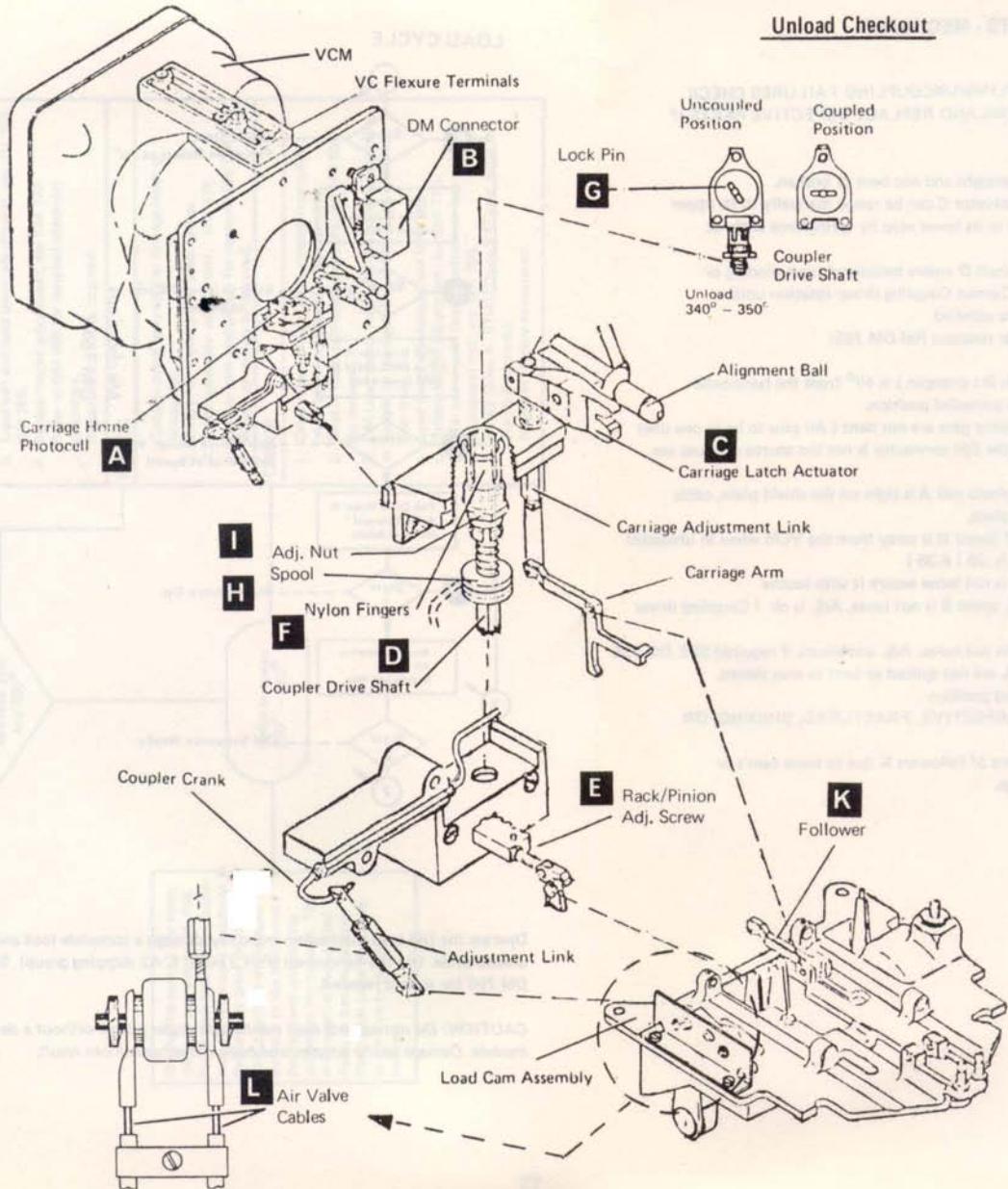
1. Data Module Functions - DM 610-611
2. Load Mechanics - DM 620-621-622
3. Load Checkout Without Data Module - DM400-401-410-420-430
4. Unload Checkout - DM 510-520
5. Bobbin Removal When Bobbin Fails To Uncouple - DM847
6. Stuck Data Module Removal:

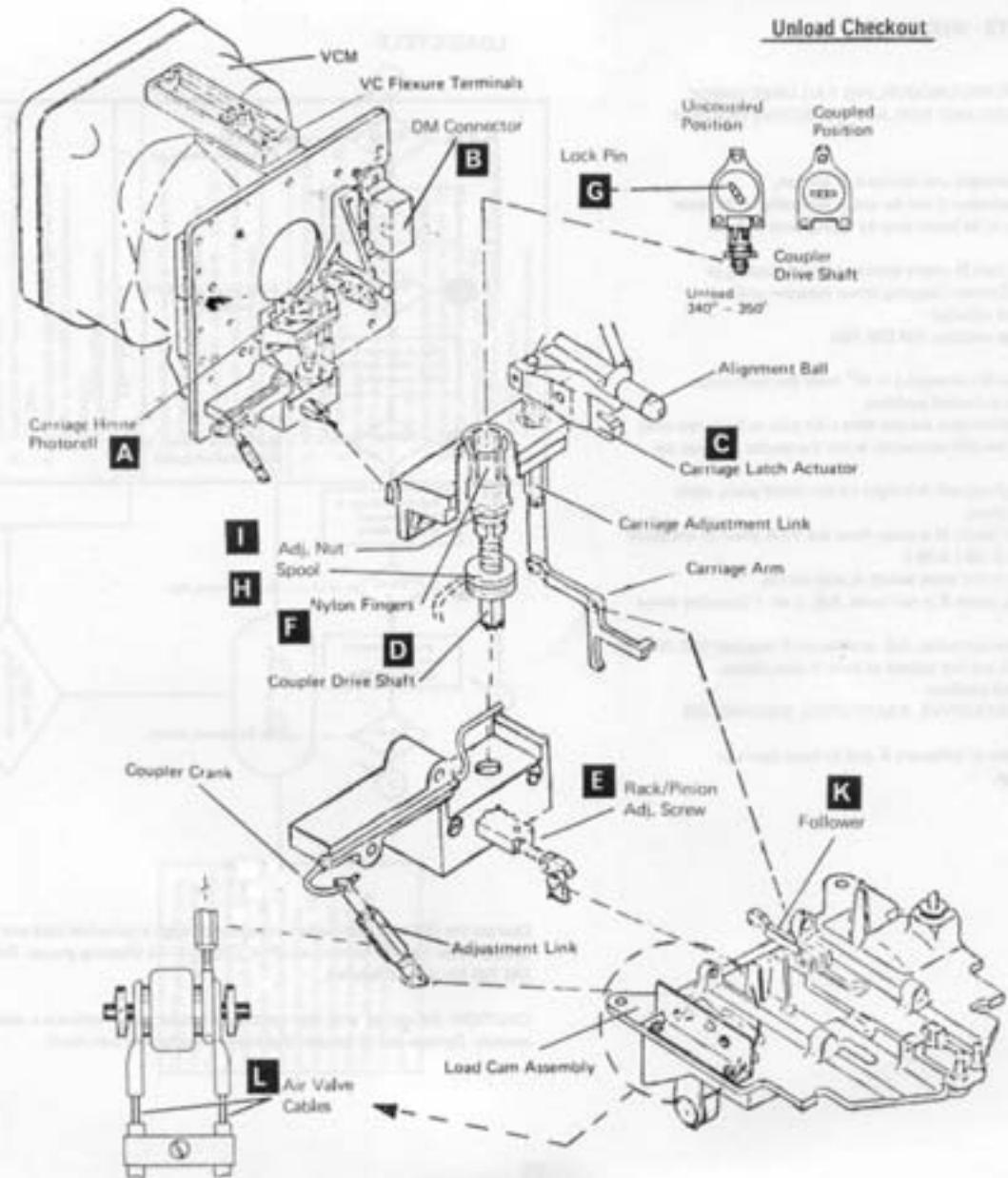
Mechanical Bind, Unloading - DM 352, 353, 500, 501  
Mechanical Bind, Loading - DM 211, 400

MECHANICAL PROBLEMS – SERVICE APPROACH FLOW CHART



## Unload Checkout





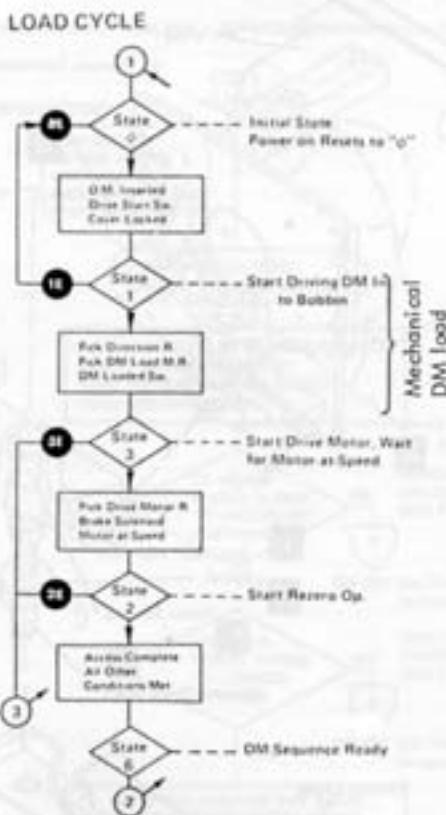
SERVICE HINTS - MECHANIC

TO AVOID COUPLING/UNCOUPLING FAILURES CHECK  
FOLLOWING ITEMS AND REPLACE DEFECTIVE PARTS IF  
NECESSARY.

- Nylonfinger F straight and not bent or broken.
  - Carriage latch actuator C can be raised manually to its upper stop and moves to its lower stop by springforce without binding
  - Coupler driver shaft D enters bobbin without binding or lifting bobbin. Correct Coupling driver rotation until requirements are satisfied  
( Coupling driver rotation Ref DM 765)
  - Coupling lock pin G ( crosspin ) is  $45^{\circ}$  from the horizontal when drive is in unloaded position.
  - Interface Connector pins are not bent ( All pins to be in one line) Make sure that the DM connector is not the source of cause see DM 870
  - Carriage home photo cell A is tight on the shield plate, cable connector is in place.
  - Deep portion of Spool H is away from the VCM when in unloaded position, depth is .25 ( 6.35 )
  - Adjusting nut I is not loose secure it with loctite
  - Pack/Finion adj. screw E is not loose, Adj. is ok ( Coupling driver rotation).
  - Load beam screw not loose, Adj. and secure if required SEE DM 785
  - airvalve cables L are not spliced or bent in area shown.  
Check on Loaded position

**CHECK FOR DEFECTIVE, FRACTURED, BINDING, OR WORN PARTS**

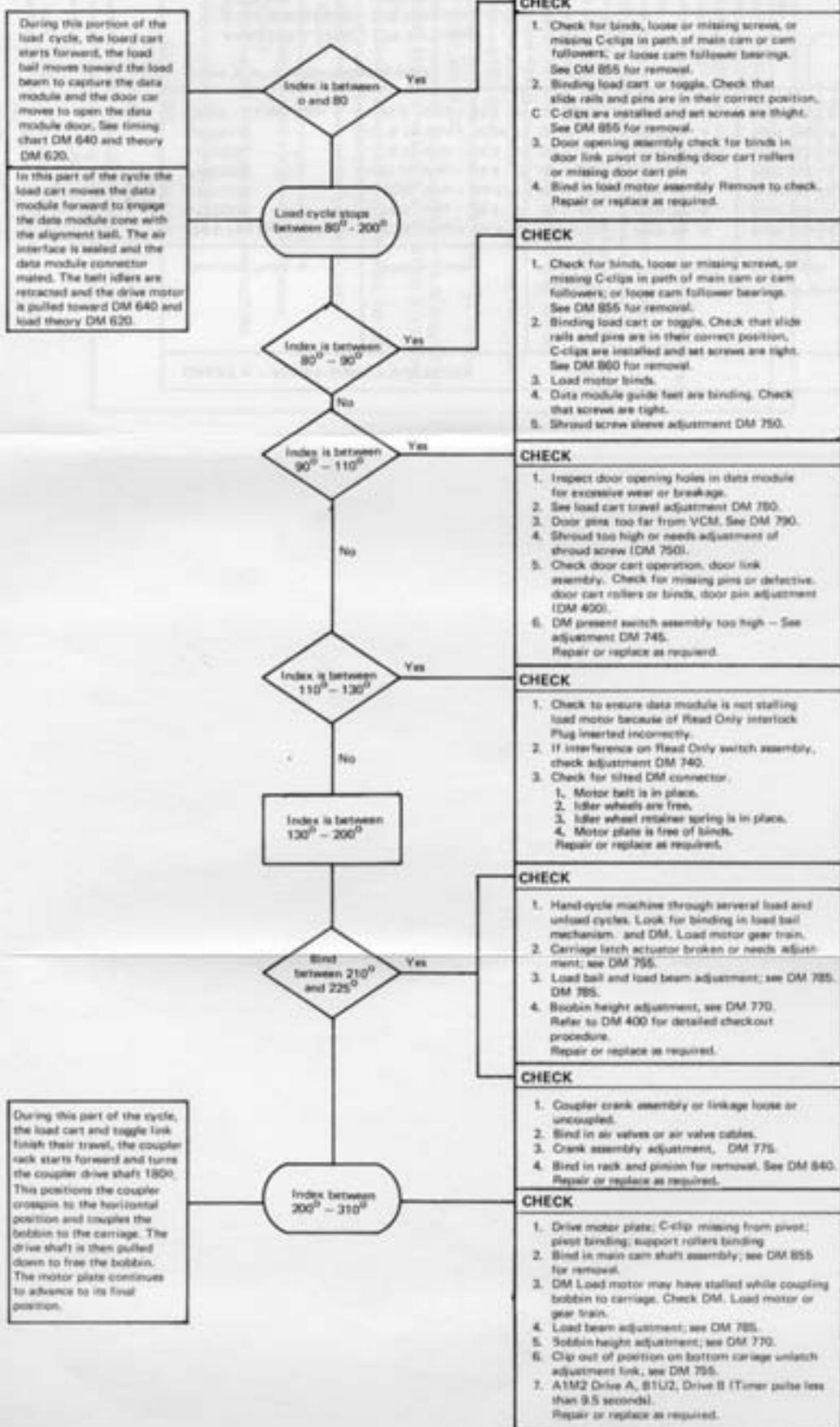
  - Accessive clearances of followers K due to loose cam's or follower bearings.



Operate the DM load mechanism manually through a complete load and unload cycle. Use the handwheel (P/N 2745011, A2 shipping group). See DM 705 for shroud removal.

**CAUTION:** Do not operate load mechanism under power without a data module. Damage to the coupler and bobbin mechanism can result.

## Load - Checkout:



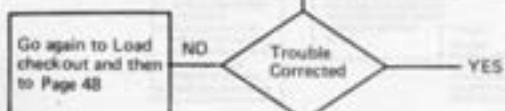
## CHART A - POWER SUPPLY VOLTAGES

Measuring Point	Voltage Range	Maximum Ripple	MAP Exit	CTRL Module	Sat Module
+24 V Per Serv. Circ. Term	+ 21.6 V to + 26.4 V	.375 V p/p	PWR 70 - A	PWR 270 - A	
A112D03	- 24.0 V to - 28.8 V	.08 V p/p	PWR 50 - A	PWR 250 - A	
A112005	+ 12.0 V to + 14.4 V	.08 V p/p	PWR 45 - A	PWR 245 - A	
A1M2D06	- 12.0 V to - 14.4 V	.08 V p/p	PWR 40 - A	PWR 240 - A	
A1P2B06	- 3.72 V to - 4.4 V	.04 V p/p	PWR 55 - A	PWR 255 - A	
A1ZB11	+ 5.76 V to + 6.24 V	.04 V p/p	PWR 60 - A	PWR 260 - A	
TB101 - 3 Logic Gate	- 36.0 V to - 43.2 V	.12 V p/p	PWR 80 - A	PWR 280 - A	

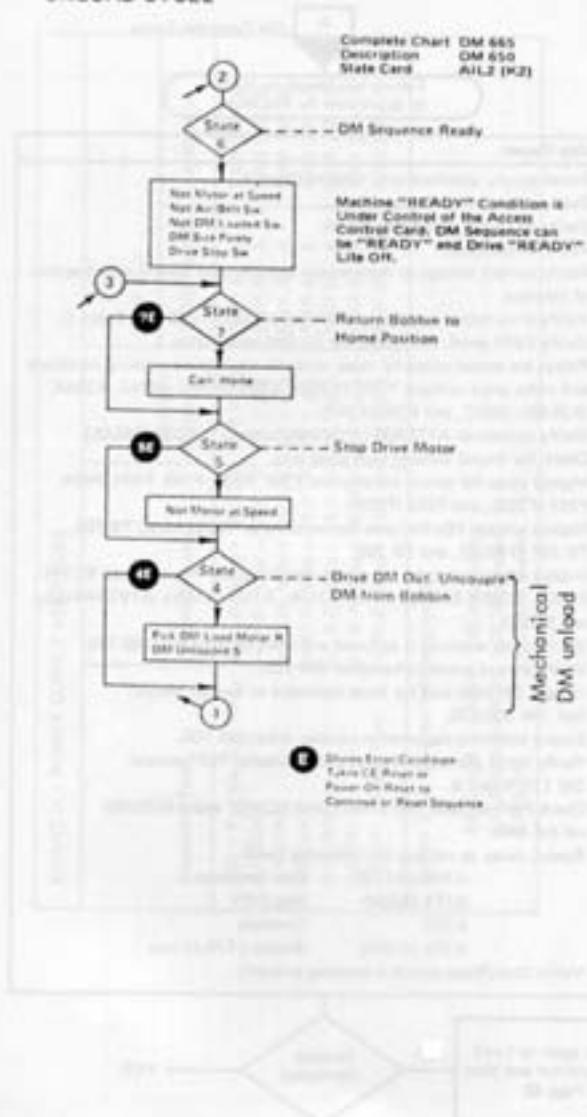
Ensure V ref is on the following cards

- A1M2 808 (A1L2) - 1.28 Vdc (nom),
- A1T2 808 (A1S4 808) - 1.28 Vdc (nom),
- A1C2 808 - 1.28 Vdc (nom).

## LOAD FAILURE



#### UNLOAD CYCLE



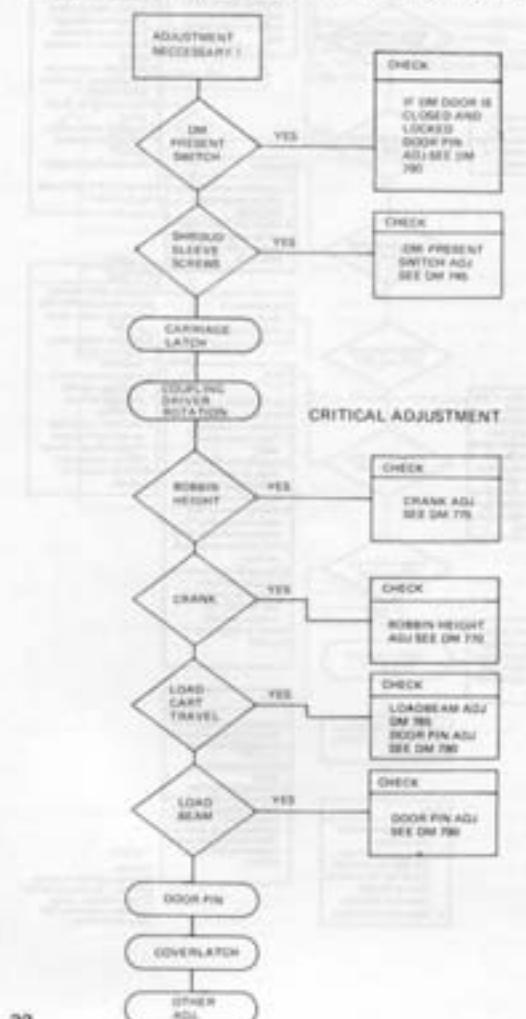
UNLOAD - CHECKOUT



## ADJUSTMENTS:

Readjustments not due to parts replacement are seldom necessary and should be carefully analyzed:

- Recheck carefully before decision is made that adjustment is needed.
- Check for defective, fractured, binding, or worn parts. The adjustments may change in effective area due to one of the preceding conditions.



## 3340 - FACTORY/FIELD- FEATURE, INSTALLATION and REMOVAL



## FEATURES FOR THE 3340



### 3340 OPTIONAL FEATURES

#### ROTATIONAL POSITION SENSING - RPS

Features # Factory B/M # Field B/M # Man hrs/Mach hrs/Sys hrs

A02/B02 = 6202	2745302	2745350	1.0	0.2	0.5
B01 = 6201	2745302	2745350			

Provides A 1G2 card

(Do not plug card with out feature - Legal exposure)

#### STRING SWITCH - SSW (See NOTE 1)

Requires 9641 on attachment for program switching;  
requires 958X on 3340 (See chart).

Feature # Factory B/M # Field B/M # Man hrs/Mach hrs/Sys hrs

A02 = 8150	2745303	2745352	4.5	3.5	0.8
B02/B01 = 9570	2745304	2745354	1.0	0.2	0.8

Provides A1J4 card in each drive and D2, E2, H2, J2, M2 in the Controller

Note: This B/M is required on each B01 and B02 in the same string  
when the A02 has String Switch Feature 8150.

#### REMOTE SWITCH ATTACHMENT

Prerequisite Feature B150 - SSW

Feature # Factory B/M # Field B/M # Man hrs/Mach hrs/Sys hrs

A02 ONLY = 6148	2745305	2745356	3.5	2.5	0.8
-----------------	---------	---------	-----	-----	-----

#### INTERMIX 3330/3340

No features required on 3340 - Functional micro code

3340 minimum hardware level EC 442903.

#### FIXED HEAD FEATURE - FHF (See NOTE 2)

Any or all 3340's may have FHF.

Feature # Factory B/M # Field B/M # Man hrs/Mach hrs/Sys hrs

A02/B02 = 4301	2757990	2757980	4.5	3.7	0.6
B01 = 4302	2757985	2757975	3.5	2.7	0.6

(Pre req E/C 442905 and up)

NOTE 1 - 8150 is not required on 3340, even though storage control has String Switch micro code. If 3340 has 8150 String Switch attachment, all storage control units attached must have 9641, SSW microcode to support.

NOTE 2 – 4301 is not required on 3340, even though storage control has Fixed Head micro code. If any 3340 has 4301 (FHFE), the storage unit must have 9190 Fixed Head micro code.  
 3830, ISC with 9190 – 2 Channel Switch only  
 No 4 channel switch micro code.

## 3348 DATA MODULE

The CE's responsibility toward 3348 data modules is:

1. Problem determination:
  - isolation of the failing unit to be within the 3348 data module
  - determination of physical damage to covers etc.
2. Repair

Field repair of data modules is limited to the replacement of:

- electrical connector (standard, P/N 2746096; FHF P/N 2745960)
- read only flipper (P/N 2746061)

## REPLACEMENT OF 3348 READ ONLY ACTUATOR

P/N 2 746 061

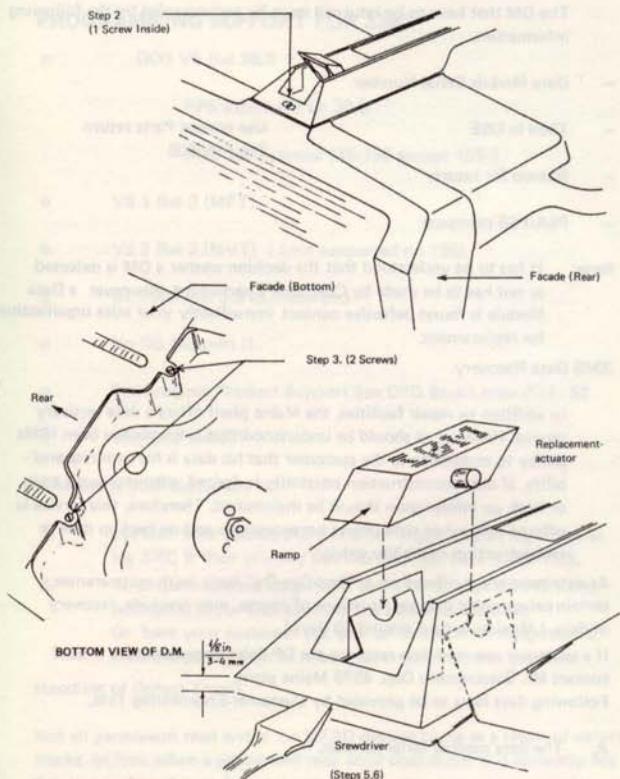
(Picture page 37)

1. Remove broken actuator.
2. Remove screw located in actuator recess.
3. Remove screws (2) holding facade on bottom of DM.
4. Remove facade.
5. Raise front edge of handle from top cover approx. 1/8 inch (3-4 mm) using a screwdriver to pry between handle and top cover above the door opening.

Note: A loud cracking may be heard which comes from tearing off part of the bonding between handle and the cover. No detrimental effect will occur the DM.

6. Twist replacement actuator to engage slots in handle. Insure ramp faces door ("Read only" is upside down visible).
7. Replace facade removed in steps 4.
8. Replace screws removed in steps 2 and 3.

REAR VIEW OF D.M.  
 REAR VIEW OF D.M.  
 REAR VIEW OF D.M.



## Return of Damaged 3348 Modules

Data modules which can not be repaired in the field are to be returned to the plant. Field repair of data modules is limited to the replacement of the electrical connector and the read only actuator.

The DM that have to be returned must be accompanied by the following information.

- Data Module Serial Number
- Time in USE Use normal Parts return Tag F/N 508
- Reason for return
- PSA/PSB printouts

Note: It has to be understood that the decision whether a DM is defective or not has to be made by Customer Engineering. Whenever a Data Module is found defective contact immediately your sales organization for replacement.

## 3348 Data Recovery

In addition to repair facilities, the Mainz plant offers a data recovery service. However, it should be understood that it has always been IBM's policy to emphasize to the customer that his data is his own responsibility. If data reconstruction capability is desired, adequate audit trails or back up information should be maintained. Therefore, this service is offered only when data proves unrecoverable and no backup data or reconstruction capability exists.

As data recovery is offered on a "Best-Can-Do" basis, with no guarantee, certain catastrophic damage situations of course, may preclude, recovery of data. (Maximum turn around 10 days).

If a customer required data recovery the DP Sales Representative should contact Mr. Diestelmann Dep. 4578 Mainz plant,

Following data have to be provided by Customer Engineering first.

- A. The data module serial number.
- B. The nature of the defect which caused the data to be unrecoverable in the field.
- C. 370 System Model ( 3125, 3135, etc.)
- D. Operating system type and level.
- E. Type of 3340 attachment ( DDA, IFA, ISC, 3830-2 ).

Recovered Data will be returned to the customer as follows:

Rental Unit - Data written on mechanical replacement data module.

Purchase Unit - Data written on customers repaired data module.

## PROGRAMMING SUPPORT FOR 3340

- o DOS VS Rel 29.0 RPS supported on 30.0
- o DOS for systems 115-158 except 155-1
- o VS 1 Rel 3 (MFT)
- o VS 2 Rel 2 (MVT) 3 (not supported on 135)
- o OLTSPP Rel 5.0 (6.0 current)
- o No OS Support !!
- o For Program Product Support See DPD Blue Letter P73 - 52
- o VM/ 370 Rel 2.1

Encourage your customers to:

- 1. Sys Gen their backup Sys Res with RES=YES to log errors in log XRC if their primary Sys Res does not have RMS=YES.
- 2. To prepare address assignment decks for changing 3340 device addresses. (Your S.E. can help the customer do this). Or have your customer sys-gen generic address assignments. This will permit him to re-address drives from the console.

## Handling of Defect Tracks

Not all permanent read errors on DASD devices come as a result of defect tracks. In fact, when a permanent read error does occur it is normally not due to a surface defect, but to a temporary write error, as a result of a transient condition. Nevertheless, a permanent read error has occurred and the user must determine whether or not the surface is usable. If he cannot write data on the track and read it back properly, that is a good indication the surface is defective. If the condition of the surface is marginal, however, the user may well be able to write data and read it back, whereas he would flag that track if all the facts were known to him. Therefore, the desirable procedure to follow in the event of a hard read error would be to first attempt to write and read back from known information on that track. If the data cannot be correctly read back, then obviously the track should be flagged. If it can be read back

correctly several times, then the track should be left unflagged and used. However, the fact that the error did occur should be noted. If over a period of time a particular track on a particular pack shows a history of failures, then probably the track itself is marginal and should be flagged; If, on the other hand, no history of errors develops, this indicates that a track is in all probability perfectly good, and a transitional write error occurred.

Some things to note:

1. The nature of ECC is such that errors may occur when processing large blocks that cannot be duplicated when processing smaller blocks, since ECC can handle one 3-bit error burst per record and smaller blocks may tend to separate two error bursts into separate records.
2. There is no surface analysis support for 3330 or 3340 in IBM software simply because it is not within the capability of the drives to perform more than cursory surface analysis which could be misleading in light of the previous discussion. Keeping a history of failures by track is the only sound approach if one desires to minimize alternate track assignment. There is, however, an undocumented capability in the VS utilities LEHHDASDR and IBCDASDI which will analyze 3340 tracks which have been flagged defective.

These programs will do a cursory surface analysis on tracks flagged defective and unflag the track if possible.

#### ALTERNATE TRACK ASSIGNMENT

**NOTE:** The following information is for your information only. It is the customer's responsibility to use utility programs to format his data modules, and to assign alternate tracks.

#### DEFINITIONS

##### Alternate Track

An alternate track is an extra track that can be used in place of a defective track. The 35 Mb data module has 12 alternate tracks, and the 70 Mb data module has 24. These alternate tracks are designated at the plant and are logically addressed as follows:

35 Mb — Cyl 348, Hd 00-11	(Decimal)
70 Mb — Cyl 696-697, Hd 00-11	(Decimal)

Unassigned alternate tracks contain their own logical track address in the CCHH bytes of the HA and RD count field. In addition, bit 7 of the Flag byte in the HA field is set to identify the track as an available alternate.

#### Defective Track

A defective track is a track with one or more surface defects which cause Read Data checks. When the track is assigned as defective, bit 6 in the Flag byte of the HA field is set, and the RD count field is written with an address pointing to an alternate track.

#### ASSIGNMENT OF AN ALTERNATE TRACK

1. Bit 6 of the Flag byte in the Flag field on the defective track is set.
2. RD count field on the defective track is written with the CCHH bytes equal to the address of the assigned alternate track.
3. The count field of RD on the alternate track to be assigned is written with the CCHH bytes equal to the address of the defective track. (That is, it points back to the defective track).

#### OS/VSE UTILITIES (Reference OS/VSE Utilities, GC35-0005)

General utility programs are available with OS/VSE operating systems to aid in alternate track assignment and data module initialization. Because alternate track assignment is a customer responsibility, the following is for information only.

#### IBC DASDI (Component J741 - SC1-11)

An independent or standalone utility used for initializing a disk pack or data module:

- o Flag track defective (Save SD).
- o Assigns alternate tracks for all tracks flagged defective.
- o Write volume label on track 0, record 3.
- o Constructs and writes a volume table of contents (VTOC), on cylinder 0 track 0.
- o Write an IPL record on track 0.
- o Allows tracks flagged defective to be analyzed and reclaimed if they are good. (Release 7.77 and above!)
- o Allows alternate track assignment for any track, whether it is defective or not. (Tracks with defects in HA or RD are included.)

## IEH DASDR (OSVS1 - R3)

An OS/VS system utility used to initialize a data module:

- o Performs the same functions as IBC DASDI, except it runs under OS/VS.
- o Can be used to dump or restore the contents or a portion of the contents of a 3340 data module. (GETALT).

## IEH ATLAS

A system utility used to assign and write an alternate track when defective tracks are indicated:

- o Attempts to rewrite defective record(s) with data supplied by user.
- o Flags a track defective if it cannot be rewritten successfully.
- o Locates and assigns an alternate track.
- o Retrieves and transfers usable data records from the defective track to the alternate track.
- o Replaces bad record(s) with data supplied by the user.

## UTILITIES OS – IEHDASDR

### 3340 Data Module Initialization Procedure

The following is recommended for 3340 data module initialization in an OS account.

For a new pack or an unlabeled pack:

1. Vary offline, run IEHDASDR ANALYZE function with passes =0.
2. Vary online, run IEHDASDR FORMAT function.

For a pack with a label and VTOC:

1. Vary online, run IEHDASDR FORMAT (or analyze) function.

To assign an alternate to a defective track:

1. Vary online, run IEHDASDR GETALT function.

IEHDASDR ANALYZE will write a volume label, IPL bootstrap records, IPL test (optional) and construct the VTOC.

IEHDASDR FORMAT will perform the same functions ANALYZE and also write a standard record zero (and erase the remainder of the track) one each track on all cylinders. All defective tracks are listed and alternates assigned for defective tracks. Prior to running FORMAT, the pack must have a valid label and VTOC. Therefore, on new or unlabeled packs, ANALYZE must be run before FORMAT.

IEHDASDR GETALT will flag a selected track as defective and assign an alternate.

NOTE: There is a cursory single track surface analysis program for 3340 data modules. Once IEHDASDR GETALT has been used to flag a track, IEHDASDR or IBCDASDI (stand alone) at Release 7.77 and above, analyze function will examine the flagged track to determine if it is possible to unflag the primary track. This will prevent using up all the alternates unnecessarily. The following control card parameters may be used to recover flagged tracks.

JOB	RECOVER
MSG	TODEV=XXXX, TOADDR=CUU
DEDEF	TODEV=XXXX, TOADDR=CUU, VOLID=XXXXX, PASSES=1, BYPASS=NO
VLD	NEWVOL=XXXXXX
VTOCD	STRTADR=NNNN, EXTENT=NN
END	

WARNING: The Surface analysis capability of this program is limited. It cannot effectively isolate marginal tracks. Therefore, a log should be maintained to determine tracks causing intermittent Data Checks. These intermittent tracks should also be re-flagged as defective.

## OTHER OS/VS UTILITIES AVAILABLE FOR DIRECT ACCESS DEVICES

IBC DMPRS, an independent utility used to dump and restore data on a direct access volume.

IEB COPY, a data set utility used to copy or merge data sets.

IEB UPDTE, a data set utility used to modify an existing partitioned or sequential data set. Logical records can be replaced, deleted, renumbered, or added to the member or data set or to try to re-write data record.

IEB ISAM, a data set utility used to copy an indexed sequential data set.

OS/VS SERVICE AIDS -- Reference OS/VS Service Aids, GC28-0633.

SPZAP, a service aid program that operates as a problem program. Can be used to replace, modify, or reconstruct direct access data records that have been destroyed by an I/O error.

#### DOS/VS UTILITIES – (Reference DOS/VS System Utilities, GC33-6381)

General utility programs are available with DOS/VS operating systems to aid in alternate track assignment and data module initialization. Because alternate track assignment is a CUSTOMER RESPONSIBILITY, the following is for information only.

##### Initialize Disk (INTDK)

A system utility used to initialize a data module:

- o Reads and verifies HA and RD fields.
- o (IQ) Write volume label on track 0, record 3. (After reading all HA and RD OK)
- o (IS) Constructs and write a volume table of contents (VTOC) on cylinder 0, track 0.

NOTE: Alternate tracks are not assigned for tracks flagged defective. It is assumed that any track that is flagged defective has previously had an alternate assigned. To assign an alternate track, use ALTDK or standalone DASDI. Has no track analyze function.

##### Assign Alternate Track Disk

##### Assign Alternate Track Disk (ALTDK)

A system utility used to assign and write an alternate track:

- o Flags defective track. (Save SD)
- o Locates and assigns alternate track.
- o Retrieves and transfers usable data records from the defective track to the alternate track.
- o Replaces bad record (z) with data supplied by the user.
- o The condition of the defective track is not analyzed.

NOTE: Alternate tracks cannot be assigned for defects in Home Address or RD count fields (use standalone DASDI 7.77 or above); cannot move HA (float) down the track.

Tracks flagged defective cannot be reclaimed with DOS (use standalone IBC DASDI 7.77 or above). No track analyze function.

#### OTHER DOS/VS UTILITIES AVAILABLE FOR DIRECT ACCESS DEVICES

##### Clear Disk -

- o To establish preformed tracks (clear) on one (one track) or more extents on a 3348 disk
- o Create a file label in the VTOC

##### Copy and Restore Disk -

Copy volume or file to disk, tape or card

##### Fast Copy Disk Volume – (present version requires 12 PTFs, all are available).

To copy entire contents of a 3348 volume to another 3348 or tape. (multi files)

##### DOS/Ditto –

A general purpose utility program can copy files from disk to disk or tape or print.

#### UTILITY PROGRAMMING PROBLEMS

##### IBCDASDI

Problem: Hangs in Test I/O loop if Data Check occurs in HA.

Status: Pre-assigned APAR # 56821.

Problem: Tracks with no defects, but flagged defective, cannot be reclaimed.

Status: Pre-assigned APAR # 56821

Problem: Documentation (OS/Vs Utilities, GC 35-0005) does not contain information on reclaiming defective tracks.

Status: Pre-assigned APAR # 56822

Problem: No adequate means exist to ship a standalone version of DASDI to the field.

Status: Will be released on P0631.

##### INITIALIZE DISK (INTDK DOS/VS REL 29)

Problem: Will not write Vol. ID on DM that has Data Check in any HA or RD field.

Status: DOS System House asked to re-asses this function.

Problem: Program turns on Security Bit with VTOC option, DASD OLTS will not run.

Status: APAR # DY 04293.

#### ASSIGN ALTERNATE TRACK DISK

Problem: Alternate track cannot be assigned for a track with a defect in the HA or RO field.

Status: APAR # DY04292 - Duplicate or APAR # DY00097.

Problem: Alternate tracks cannot be assigned on volumes without a Volume ID.

Status: DOS System House asked to re-asses this function.

#### SOFTWARE FIXES FOR 3340 HARDWARE SYMPTOMS

SYMPTOM	SYSTEM	APAR
---------	--------	------

INTKD turns on security bit in 3340 VTOC  
DASD OLTS will not run

DOS      DY04293

Can't assign alternate tracks if error  
in HA or RO

DOS      DY04292

Partial track flagging in 3340. SD bytes  
set to 0

DOS      DY00097

Job canceled due to not testing device  
busy condition

DOS      E00164

Loop on CUEND/CUBUSY when running ILTS  
on 3115

DOS      E02152

Invalid seek address generated when  
running sort in virtual mode

VS      P22430

Command reject on seek command due  
to incorrect test for device type

701534

MSG DP711 issued if extents allocated  
larger than necessary

DOS      D01179

Command reject on IPL

DOS      E02159

ERP don't log INT.. REQ.

DOS      E03237

STAT MOD and busy from SIO

DOS      DY03238

INIT disk writes wrong F4 label

DOS      DY01288

IFA channel checks not edited  
for 3340

DOS      E01325

Data checks not printed intermittent

DOS      E03131

Fast copy cancels with MSG BF2BA

DOS      E02888

No record found on compiler

VS      P21366

MSG BF2BA during fast copy

DOS      DY02157

Chan PGM check on skip displacement

DOS      E02753

invalid statistical data in EREP

DOS      E00079

Soft overruns being recorded  
(only permanent overruns should  
be logged)

DOS      E00082

Volume statistics summary not printed

DOS      E00080

Invalid data in EREP

DOS      E00060

Wrong length record logged in EREP

DOS      E00062

Write inhibit record logged

DOS      E00081

## APPROACH TO INTERMITTENT 3340 PROBLEM (CHECKLIST)

### BE METHODICAL

1. Get it resolved so it does not come back to plague you! (Look for trouble).
2. Take an action! Take some corrective action, eliminate a logical part.
3. Card swapping - keep a log on a tab card - attach to machine.
4. Loop a microprogram test likely to fail (use with step 5).
5. Moderate vibration in suspected trouble area or to SLT gate may be effective. Rake cards and pins with folded tab cards.
6. Voltage - Check AC ripple on DC voltages. If exceeds specification take corrective action. See PWR 90.

Nominal + 6.0V TP = D1AA1J2B11  
+/- 5% = +6.30V to 5.77V

Nominal - 4.0V TP = D1AA1J2B06  
(Not adjustable in B1/B2 boxes)  
+/- 5% = -4.20V to -3.77V

7. VCM temperature should not exceed 79 degrees C. Check fans and filters - location and blockage. See MLM page DM 630.

1. Main intake filter behind MST gate, under machine at DC box. Keep cables, paper, and cards away. Check the filter on every call, and clean at least every 6 months.
2. Heat exchanger - insure that air duct is in place and good air flow exists across the heat exchanger, to cool the VCM with recycled air through the absolute filter. Check that air is blowing out top of gate. (DC compartment and A1 A2 boards covered). The operating temperature of the MST cards may be raised by removing the cover from the DC compartment. (Do not leave DC compartment cover off permanently). Exhaust air from the MST logic gate must not exceed 49 degrees C.

### NOTE:

The AC box is NOT air cooled and may run above 43 degrees C.

8. Loose screw/wire crimp audit - power off  
AC box  
DC bulk box  
DC regulation box  
Gate terminal blocks
9. Look for shorts/ground - cold flow, tight wire wraps, bent pins or stray lengths of stripped wire. Check base plate grounding.
10. Visual check of cables for chafing, grounding, strain
11. Gather information - core dumps, sense info, log rec, console printouts, OLTs and ST370 errors.
12. Keep a log of failures: FSI, micro, drive, data module S/N.
13. Try to simulate the failure, - any MST 1 line (except Servo Analog signals) may be tied to ground, to force a +MST 1 level.
14. Scope theorized trouble spot - use a systematic investigation. Know what you expect to see.
15. Pulse check signal lines in suspected area.
16. Use baby-sitting devices: probe, latch card, scope DIGITEK.
17. Use all documentation - CEM, RETAIN, MM, MLM, System TMM, SRL. Call your district or FSG and search the hardware and software data bank.
18. Get away from it all - cup of coffee.
19. Look for noise - scope AC voltage input for noise scope or voltage lines for ripple and noise when customer running scope sensitive circuits. For example: PLD, R/W Data.
20. DC voltages can only be set with digital voltmeter - critical DC voltages + 6.0V and - 4.0V (leave digital voltmeter on voltage to monitor during failure).
21. Don't overlook environment static, electrical noise generators, radar, power line dips and/or transients, and etc.
22. Interchangeability - change entire sections of cards.  
Example: SERVO is contained on 4 cards and Power Amp.
23. Swapping - circuit swapping on higher level - interchange A1 board between drives.
24. Interchange VCMs

APPROACH TO INTERMITTENT FAULTS AND VIBRATION PROBLEMS

25. Unnecessary parts replacement - reinstall original part ASAP when trouble fixed (use card kit).

26. Use phone-call for assistance from district /FSG. Open a RETAIN incident so that the ASG and the PLANT can assist you.

27. Customer relations - keep customer informed of your progress during the problem definition and analyses period.

28. Lower temperature of MST 1 cards with FREON ( thermal shock).

3340-GATE TB 101

1	} GATE THERMAL IN "B" - PWR	214
2		
3	-36V PWR 01+201	
4	-24V PWR 51+251+201	
5	+6V. PWR 61	
6	-12V. PWR 41	
7	+12V. PWR 41	
8	+12V RETURN PWR 41	
9	+6V. " PWR 17+61	
10	-4V. " PWR 17. PWR 56.	
11		
12	} PWR 17 13 PWR 56	
14		36V RETURN PWR 81
15		
16	24V RETURN PWR 51+251	
17	n.c OF +6V. PWR 61.	
18	n.c.	
19		

## NOTES

PLO VFO CARD AT - A2T2

PART N° # 8238240.

CHANN. 1 to A2T2 J025

" 2 to A2T2 B08

VREF. CHAN 2

VFO SS CHAN 1

TRACES AT THE SAME  
GROUND LEVEL.

THERE MUST NOT BE RUNNING  
ANY JOB DURING ADJUSTM.

3340 W.T. TIP 039.



*Prepared by*  
IBM Dept. 7906 Mainz  
Sept. 1975

*Printed and distributed by*  
IBM Dept. 7902  
7000 Stuttgart 80  
P.O. Box 800880

*Order Nr. 7902-128*